

Brief Biography:



Professor Eduard Babulak is accomplished international scholar, researcher, consultant, educator, professional engineer and polyglot, with more than thirty years of experience. He served as Editor, reviewer, Conference Chair, has successfully published. He served as Chair of the IEEE Vancouver Ethics, Professional and Conference Committee.

He was Invited Plenary & Keynote Speaker in Tokyo (2023), Oxford (2020 & 2023), University of Cambridge (2005 & 2010), MIT (2005 & 2010), Yokohama National University & University of Electro Communications in Tokyo (2009 – 2010), Kyushu Institute of Technology (2016), Japan, Shanghai Jiao Tong University (2013), Sungkyunkwan University in Korea (2012 – 2014), University of Surrey, Purdue University, Penn State, Graz University of Technology, Austria, Czech Technical University, and other prestigious academic institutions worldwide.

His academic and engineering work was recognized internationally by the Engineering Council in UK, the European Federation of Engineers and credited by the Ontario Society of Professional Engineers and APEG in British Columbia in Canada. He was awarded higher postdoctoral degree DOCENT - Doctor of Science (D.Sc.) in the Czech Republic, Ph.D., M.Sc., and High National Certificate (HNC) diplomas in the United Kingdom, as well as, the Diploma Engineer (MSc and B.Sc.) in Electrical Engineering in Slovakia.

He is Fellow of the Royal Society RSA, London, UK; Chartered Fellow. Mentor and Member of the ELITE Group of the British Computer Society, London, UK; 2013-2014 Invited Panel Member for National Science Foundation, USA; Expert Consultant for HORIZON 2020 & CORDIS FP6 - FP7 European Commission, Brussel, Belgium; Mentor and Senior Member of the IEEE and ACM, USA; Nominated Fellow of the Institution of Engineering and Technology, London, UK and Distinguished Member of the ACM, USA; Chartered Member of the IET, London, UK.

He serves as the Editor-in-Chief, Associate Editor-in-Chief, Co-Editor, and Guest-Editor. He speaks 16 languages and his biography was cited in the Cambridge Blue Book, Cambridge Index of Biographies, Stanford Who's Who, and number of issues of Who's Who in the World and America.

- Fellow of the Royal Society RSA, London, UK;
- Chartered Professional IT Fellow, Mentor and Elite Group Member of British Computer Society, London, UK
- Invited Panel Member for National Science Foundation Graduate Research Fellowship Program, USA
- Expert Consultant for EU HORIZON2020 & CORDIS FP6 – FP7 European Commission, Brussels, Belgium
- Mentor and Senior Member of the IEEE and ACM, USA
- Nominated Fellow of the Institution of Engineering and Technology, London, UK and Distinguished Member of the ACM, USA
- Chartered Member of the IET, London, UK
- Professional Member of American Society for Engineering Education, American Mathematical Association, and Mathematical Society of America, USA

Education:

- D.Sc., University of Pardubice (Czech Republic), 2008
- Ph.D., Staffordshire University (United Kingdom), 2003
- M.Sc., University of East London, (United Kingdom), 1991
- High National Certificate, Brighton College of Technology (United Kingdom), 1990
- M.Sc., Technical University of Kosice (Slovakia), 1982
- High National Diploma, Electro-technical College (Slovakia), 1976